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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/774,534	01/31/2001	Bing Chiang	2479.2028-000 (TAN00-18)	3250

21005 7590 10/28/2002

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EXAMINER

GLENN, KIMBERLY E

ART UNIT	PAPER NUMBER
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2817

DATE MAILED: 10/28/2002

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/774,534

Applicant(s)

CHIANG ET AL.

Examiner

Kimberly E Glenn

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 7/23/02.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☐ Claim(s) 1-34 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-3, 6-9, 12, 13, 15-18, 21-23, 26, 29, 30 and 32 is/are rejected.
- 7) ☒ Claim(s) 4, 5, 10, 11, 19, 20, 24, 25, 27, 28, 31, 33 and 34 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on _____ is: a) ☐ approved b) ☐ disapproved by the Examiner.
- If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
- a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449) Paper No(s) 7.
- 4) ☐ Interview Summary (PTO-413) Paper No(s) _____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____.

DETAILED ACTION

Claim Rejections - 35 USC § 112

1. The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter, which the applicant regards as his invention.

2. Claim 14 is rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

3. Claim 14 recites the limitation "the bias voltage port" in line 5. There is insufficient antecedent basis for this limitation in the claim.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

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Claims

~~In light of the above 35 USC 112 rejection,~~ *claims* 1-3 6-9, 12,13, 15-18, 21-23, 26, 29, 30 and 32 are rejected under 35 U.S.C. 102(b) as being anticipated by Arevalo US Pat. 6,104,986.

4. Arevalo disclose a input port (port 1) coupled to receive a input signal; an output port(port 2) coupled to provide the an output signal, the output port coupled to the input port, such coupling between the input port and the output port having a characteristic input/output impedance ; a first quadrature port(port 3) and a second quadrature port (port 4) , the quadrature port coupled to one another, , such coupling between quadrature ports having a characteristic quadrature port impedance; a first impedance transformer (112) coupled between the input port (port 1) and the a first one of the quadrature ports (port 3); and a second impedance transformer 108 coupled between the a second one of the quadrature ports(port 4) and the output port (port 2). (*Claim 1*) A branch line provides the coupling between the input port and the output port. (*Claim 2*) A branch line provides the coupling between quadrature ports. (*Claim 3*) The first and second impedance transformers are implemented as one-quarter-wavelength section of transmission line. (*Claims 6 and 7*) A varactor diode is connected to each of the quadrature ports. (*claims 8 and 9*) Arevalo disclose that the microstrip elements (108 110 112 114) have widths that correspond to the desired impedance of each leg. The four legs need not have the same width. The method steps to the above apparatus are inherent. (See figure 1 and column 2 line 43 through column 3 line 57 a)

5. Arevalo is shown to teach all the limitations of the claims with the exceptions of :

- (1) the characteristic input/output impedance being different from the characteristic quadrature port impedance.

(2) the first impedance transformer transforming the characteristic input/output impedance across the input port and output port to the characteristic quadrature port impedance across the quadrature ports.

(3) the second impedance transformer transforming the characteristic quadrature impedance across the quadrature ports to the characteristic input/output impedance.

(4) the characteristic input/output impedance is 50 ohms.

(5) the characteristic quadrature port impedance is 20 ohms.

(6) the characteristic quadrature port impedance is lower than the characteristic input/output port impedance.

One skilled in the art at the time the invention was made would have found it obvious to optimize the impedance match between the input and output ports as well quadrature ports. Therefore the characteristic input/output impedance and the characteristic quadrature impedance are not the same.

One skilled in the art at the time the invention was made would have found it obvious to have the first impedance transformer transform the characteristic input/output impedance to the characteristic quadrature port impedance in order to optimize the impedance match between the input and output ports and the quadrature ports.

One skilled in the art at the time the invention was made would have found it obvious to have the second impedance transformer transform the characteristic quadrature ports impedance to the characteristic input output impedance in order to optimize the impedance match between the input and output ports and the quadrature ports.

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One skilled in the art at the time the invention was made would have found it obvious to the have characteristic input/output port impedance be 50 ohms, since it has been held that discovering an optimum value of a variable involves only routine skill the art.

One skilled in the art at the time the invention was made would have found it obvious to the have characteristic quadrature impedance be 20 ohms, since it has been held that discovering an optimum value of a variable involves only routine skill the art.

Allowable Subject Matter

Claims 4, 5, 10, 11, 14, 19, 20, 24, 25, 27, 28, 31, 33 and 34 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

The following is a statement of reasons for the indication of allowable subject matter: With regards to claims 4, 5, 19 and 20, the prior art of record does not disclose or fairly teach the coupling between the input port and the output port (or quadrature ports) being provided by coupling lines. With regards to claims 10, 24, 27 and 33, the prior art of record does not disclose or fairly teach an input bias voltage being applied to the a varactor diode. With regard to claims 14 and 31, the prior art of record does not disclose or fairly teach a radio frequency choke being applied between a bias voltage port and one of the quadrature ports.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kimberly E Glenn whose telephone number is (703) 306-5942. The examiner can normally be reached on Monday through Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Pascal can be reached on (703) 308-4909. The fax phone numbers for the

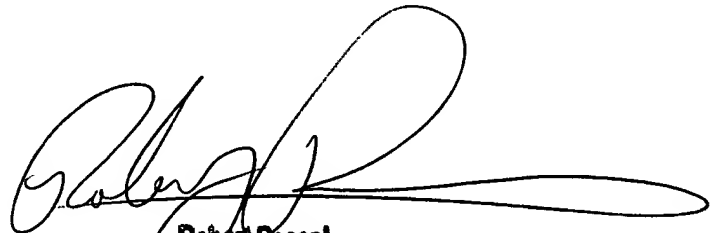
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organization where this application or proceeding is assigned are (703) 308-7724 for regular communications and (703) 308-7724 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

Kimberly E Glenn
Examiner
Art Unit 2817

keg
October 18, 2002

A large, stylized handwritten signature in black ink, likely belonging to Robert Pascal, is written over the printed name and title.

Robert Pascal
Supervisor Patent Examiner
Technical Center